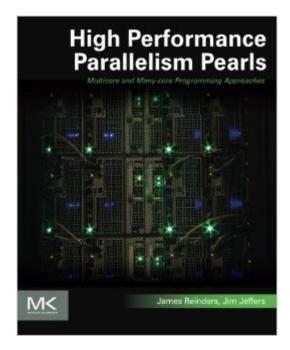
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# High Performance Parallelism Pearls Volume One: Multicore And Many-core Programming Approaches





# Synopsis

High Performance Parallelism Pearls shows how to leverage parallelism on processors and coprocessors with the same programming â " illustrating the most effective ways to better tap the computational potential of systems with Intel Xeon Phi coprocessors and Intel Xeon processors or other multicore processors. The book includes examples of successful programming efforts, drawn from across industries and domains such as chemistry, engineering, and environmental science. Each chapter in this edited work includes detailed explanations of the programming techniques used, while showing high performance results on both Intel Xeon Phi coprocessors and multicore processors. Learn from dozens of new examples and case studies illustrating "success stories" demonstrating not just the features of these powerful systems, but also how to leverage parallelism across these heterogeneous systems. Promotes consistent standards-based programming, showing in detail how to code for high performance on multicore processors and Intel® Xeon Phiâ,,¢Examples from multiple vertical domains illustrating parallel optimizations to modernize real-world codesSource code available for download to facilitate further exploration

## **Book Information**

Paperback: 600 pages Publisher: Morgan Kaufmann; 1 edition (November 17, 2014) Language: English ISBN-10: 0128021187 ISBN-13: 978-0128021187 Product Dimensions: 7.5 x 1.2 x 9.2 inches Shipping Weight: 2.5 pounds (View shipping rates and policies) Average Customer Review: 3.3 out of 5 stars Â See all reviews (3 customer reviews) Best Sellers Rank: #994,547 in Books (See Top 100 in Books) #96 in Books > Computers & Technology > Programming > Parallel Programming #488 in Books > Computers & Technology > Hardware & DIY > Design & Architecture #1257 in Books > Textbooks > Computer Science > Software Design & Engineering

## **Customer Reviews**

I bought this book expecting to learn more about effective parallelization techniques. Unfortunately, though there might really be a lot of wisdom in the each of the pearls, the amount of domain specific information, in physics, math, meteorology, etc, in each of the articles obscures the parallelization information I was interested in.

A very useful collection, with a couple exceptional examples of methods to increase concurrency / parallelism in applications. The authors have also posted all the codes and diagrams in public which make this extremely useful as a teaching device. While the codes are developed on Intel hardware, the practices are general and useful for any general purpose processor.

A solid book. You don't have to read it in sequence, it's more of a cookbook kind of thing where you can skip around and pick up pieces.

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